

## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) **Name of the Faculty:** - Mrs. Pawar Vanashri A.

2) **Date of Birth :-** 29/04/1964

3) **Educational Qualifications :-**

- a) B.E. (Electronics)
- b) M.E. VLSI Design
- c)



4) **Work Experience :-**

- a) Teaching :- 30 Years
- b) Research :-
- c) Industry :-
- d) Professional :-

5) **Area of Specialization :-** VLSI

6) **Subjects Taught at UG/PG Level :-**

- a) Linear Integrated Circuits
- b) Mobile Communications
- c) Digital Communication System
- d) Microcontroller and Applications

7) **Research Guidance :-**

a) **Number of papers published in**

- Masters's** - National Journals
- Ph.D.** - International Journals - 01
- Conference

8) **Projects Carried Out:-** 01

9) **Patents:-**

10) **Technology Transfer:-**

11) **Research Publications:-**

12) **Number of Books Published with Details:-** 01 VLSI Design Textbook  
according to MSBTE Curriculum published by Techmax Publication, Pune

## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) Name of the Faculty: - Mr. Patil Arun Prataprao

2) Date of Birth :- 20/04/1965

3) Educational Qualifications :-

a) B. E. (E & C)

b)

c)



4) Work Experience :-

a) Teaching :- 27 Years

b) Research :-

c) Industry :- Five Years (Digital System, AP Lab)

d) Professional :-

5) Area of Specialization :- .

6) Subjects Taught at UG/PG Level :-

a) Industrial Drive

b) Fundamentals of Power Electronics

c) Industrial Measurement

d) Power Electronics

7) Research Guidance :-

a) Number of papers published in

Masters's - National Journals- Nil

Ph.D. - International Journals- Nil

- Conference- Nil

8) Projects Carried Out :-

9) Patents :-

10) Technology Transfer :-

11) Research Publications :-

12) Number of Books Published with Details :-

## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) **Name of the Faculty:** - Mrs. Khobare V. K.

2) **Date of Birth :-** 09/03/1972

3) **Educational Qualifications :-**

- a) B.E. (Electronics)
- b) M.E. VLSI Design
- c)

4) **Work Experience :-**

- a) Teaching :- 19 Years
- b) Research :-
- c) Industry :-
- d) Professional :-

5) **Area of Specialization :-** VLSI Design

6) **Subjects Taught at UG/PG Level :-**

- a) Analog Communication
- b) Basic Electronics
- c) Advance Communication
- d) Fundamental of Mechatronics

7) **Research Guidance :-**

a) **Number of papers published in**

- Masters's** - National Journals
- Ph.D.** - International Journals : 01
- Conference

8) **Projects Carried Out :-** 01

9) **Patents :-**

10) **Technology Transfer :-**

11) **Research Publications :-**

12) **Number of Books Published with Details :-** 02 Basic Electronics, Power Electronics to MSBTE Curriculum published by Nirali Publication, Pune



## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) **Name of the Faculty:** - Mr. Pawar Mansing Anandrao

2) **Date of Birth :-** 01/06/1968

3) **Educational Qualifications :-**

- a) B.E.(Instrumentation)
- b)
- c)



4) **Work Experience :-**

- a) Teaching :- 22 Years
- b) Research :-
- c) Industry :-
- d) Professional :-

5) **Area of Specialization :-** Telemetry & Telecontrol

6) **Subjects Taught at UG/PG Level :-**

- a) Process Control Instrumentation
- b) Electronic Instrumentation
- c) Linear Integrated Circuits
- d)

7) **Research Guidance :-**

a) **Number of papers published in**

**Masters's** - National Journals

**Ph.D.** - International Journals

- Conference

8) **Projects Carried Out :-** 01

9) **Patents :-**

10) **Technology Transfer :-**

11) **Research Publications :-**

12) **Number of Books Published with Details :-**

## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) **Name of the Faculty:** - Mr. Patil Amitrao Subhash

2) **Date of Birth :-** 05/06/1981

3) **Educational Qualifications :-**

- a) B.E. (Electronics)
- b) M.E. (VLSI and Embedded Systems)
- c)



4) **Work Experience :-**

- a) Teaching :- 14 Years
- b) Research :-
- c) Industry :- Two Years
- d) Professional :-

5) **Area of Specialization :-** VLSI and Embedded Systems

6) **Subjects Taught at UG/PG Level :-**

- a) Digital Electronics & Microcontroller
- b) Elements of Industrial Automation
- c) Applied Electronics
- d) Embedded Systems

7) **Research Guidance :-**

a) **Number of papers published in**

- Masters's** - National Journals
- Ph.D.** - International Journals - 01
- Conference

8) **Projects Carried Out :- 01**

9) **Patents:-**

10) **Technology Transfer:-**

11) **Research Publications:-**

12) **Number of Books Published with Details:-** 02 - VLSI Design and Industrial Automation Textbook according to MSBTE Curriculum published by Techmax Publication, Pune

## FACULTY PROFILE

### BRANCH – INDUSTRIAL ELECTRONICS

1) Name of the Faculty: - Mr. Patil Ashok Dattatray

2) Date of Birth :- 17/09/1977

3) Educational Qualifications :-

- a) B.E. (Electronics)
- b) M.E. (VLSI and Embedded Systems)
- c)



4) Work Experience :-

- a) Teaching :- 16 Years
- b) Research :-
- c) Industry :-
- d) Professional :-

5) Area of Specialization :-

6) Subjects Taught at UG/PG Level :-

- a) Control Systems
- b) Power Electronics
- c) Elements of Industrial Automation
- d) Mechanical Engineering Measurements

7) Research Guidance :-

a) Number of papers published in

- Masters's - National Journals
- Ph.D. - International Journals - 01
- Conference

8) Projects Carried Out :- 01

9) Patents :-

10) Technology Transfer :-

11) Research Publications :-

12) Number of Books Published with Details :- 01 - Industrial Automation

Textbook according to MSBTE Curriculum published by Techmax  
Publication, Pune